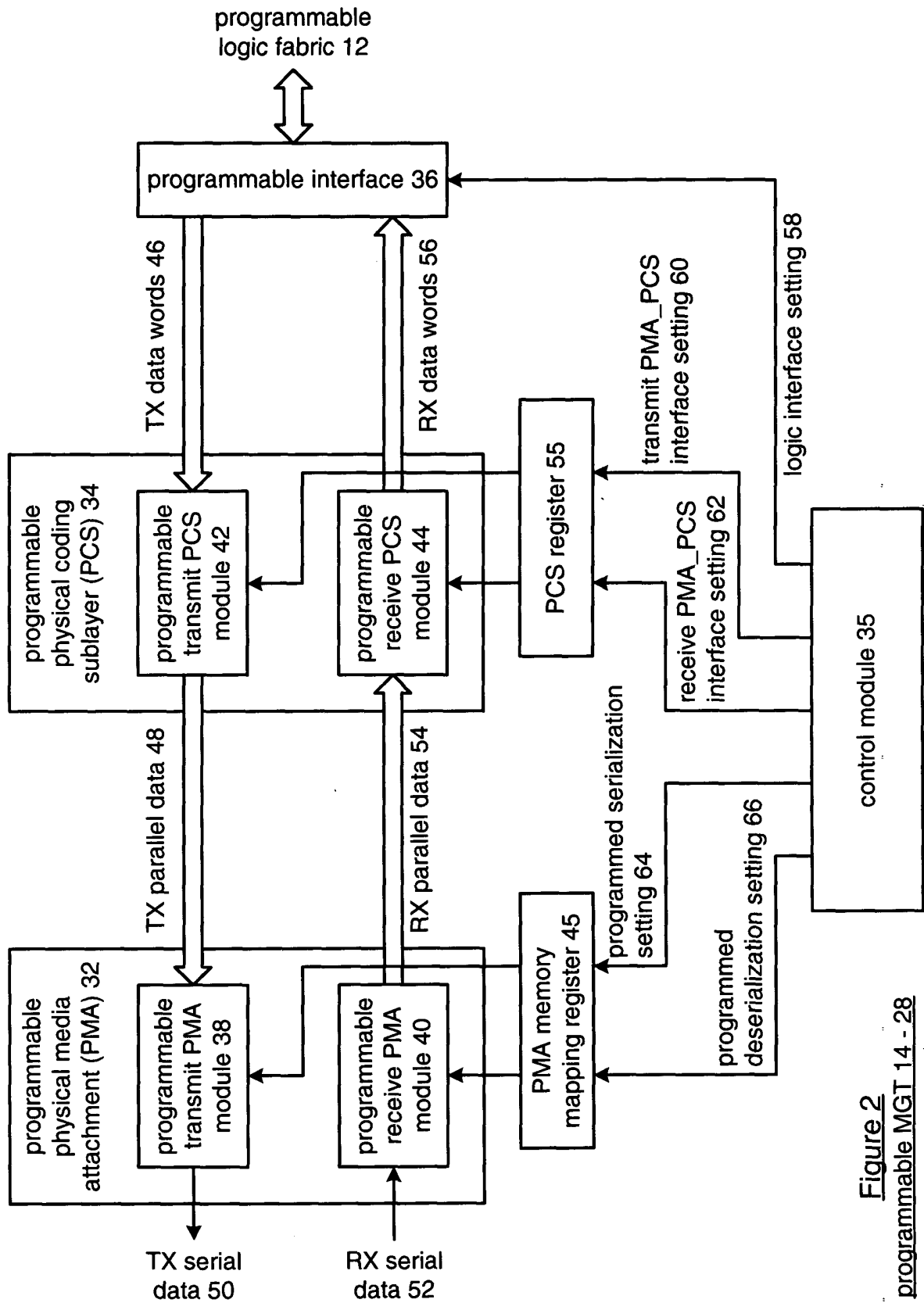
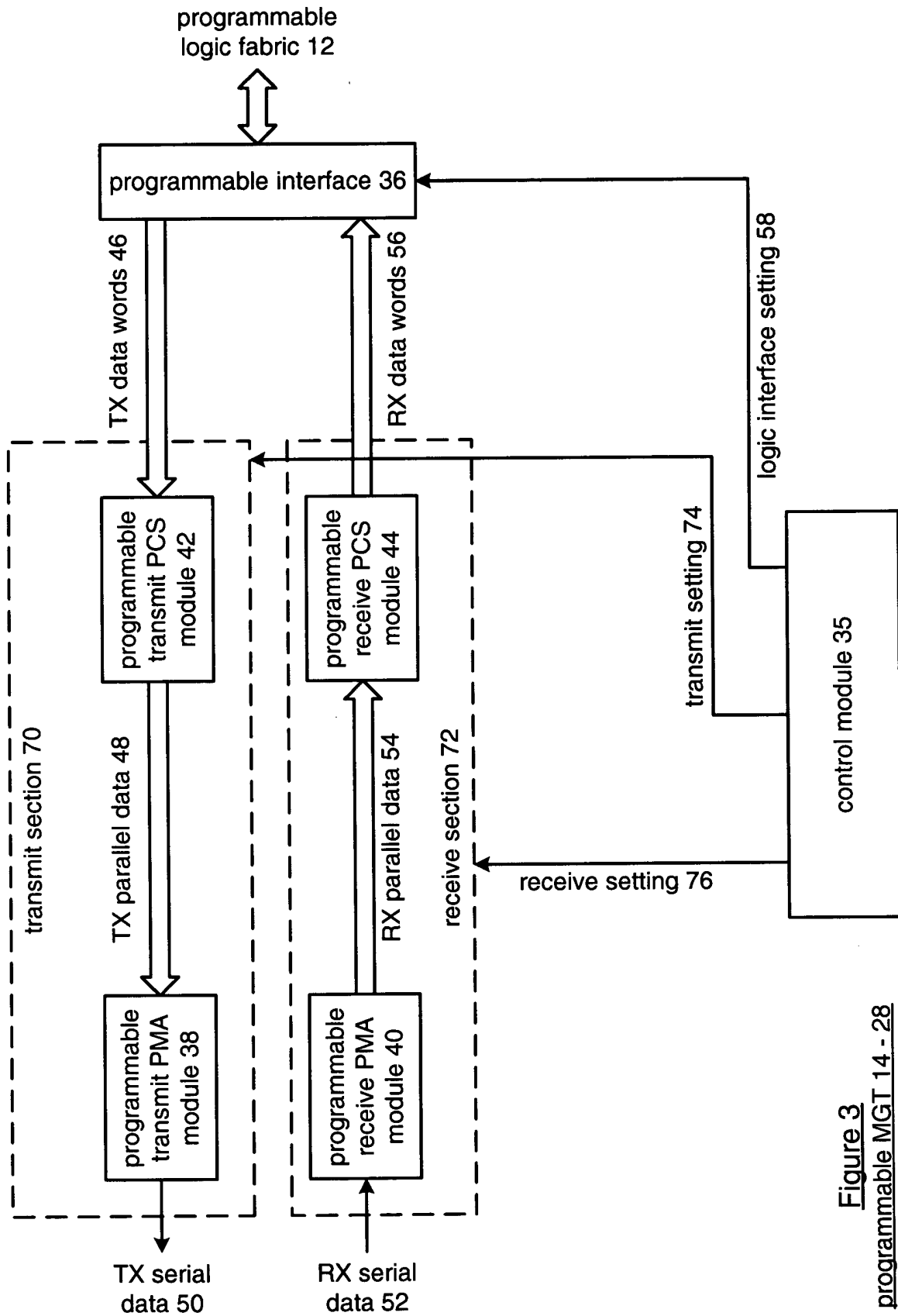


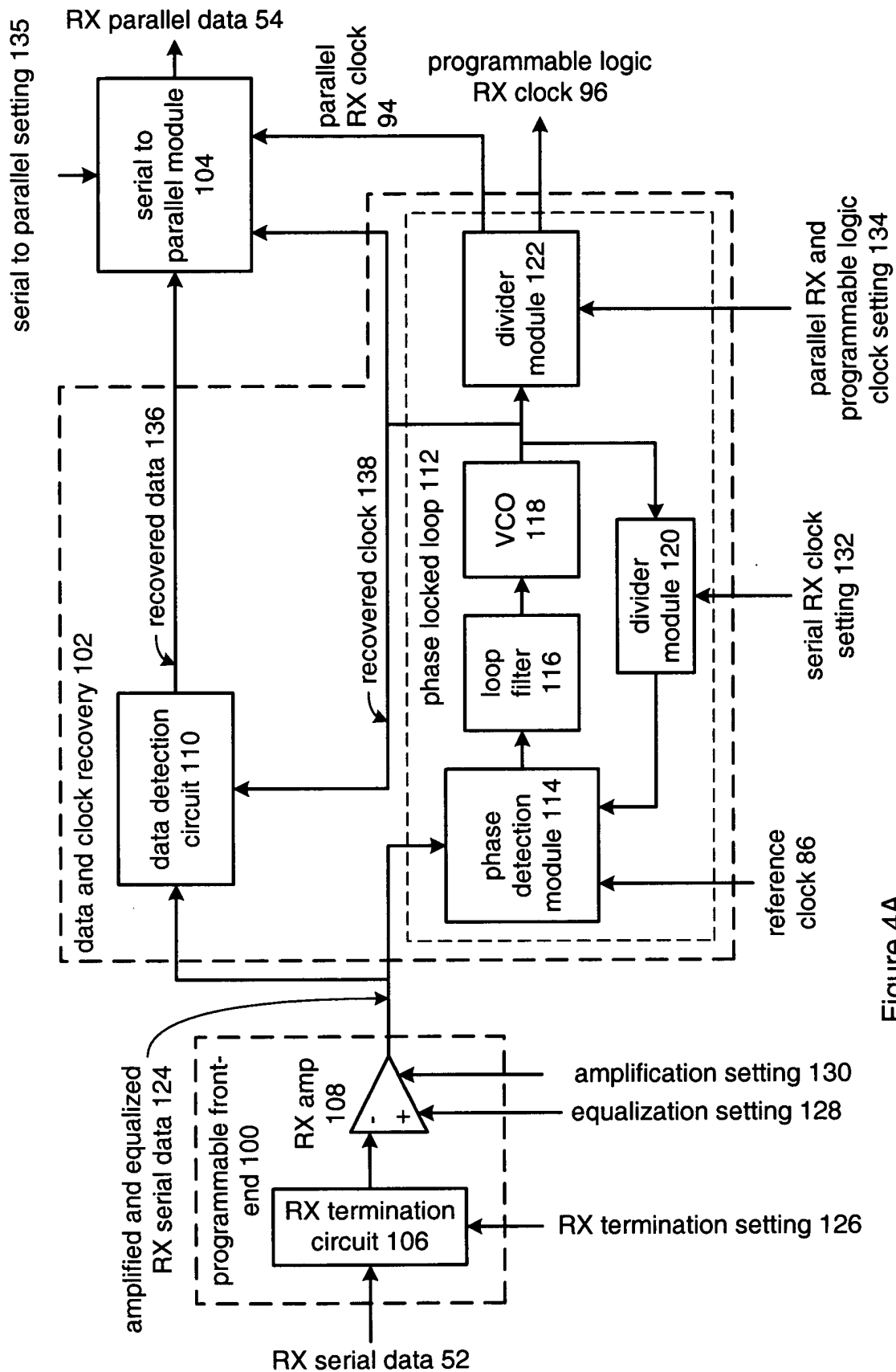
Figure 1  
programmable  
logic device 10



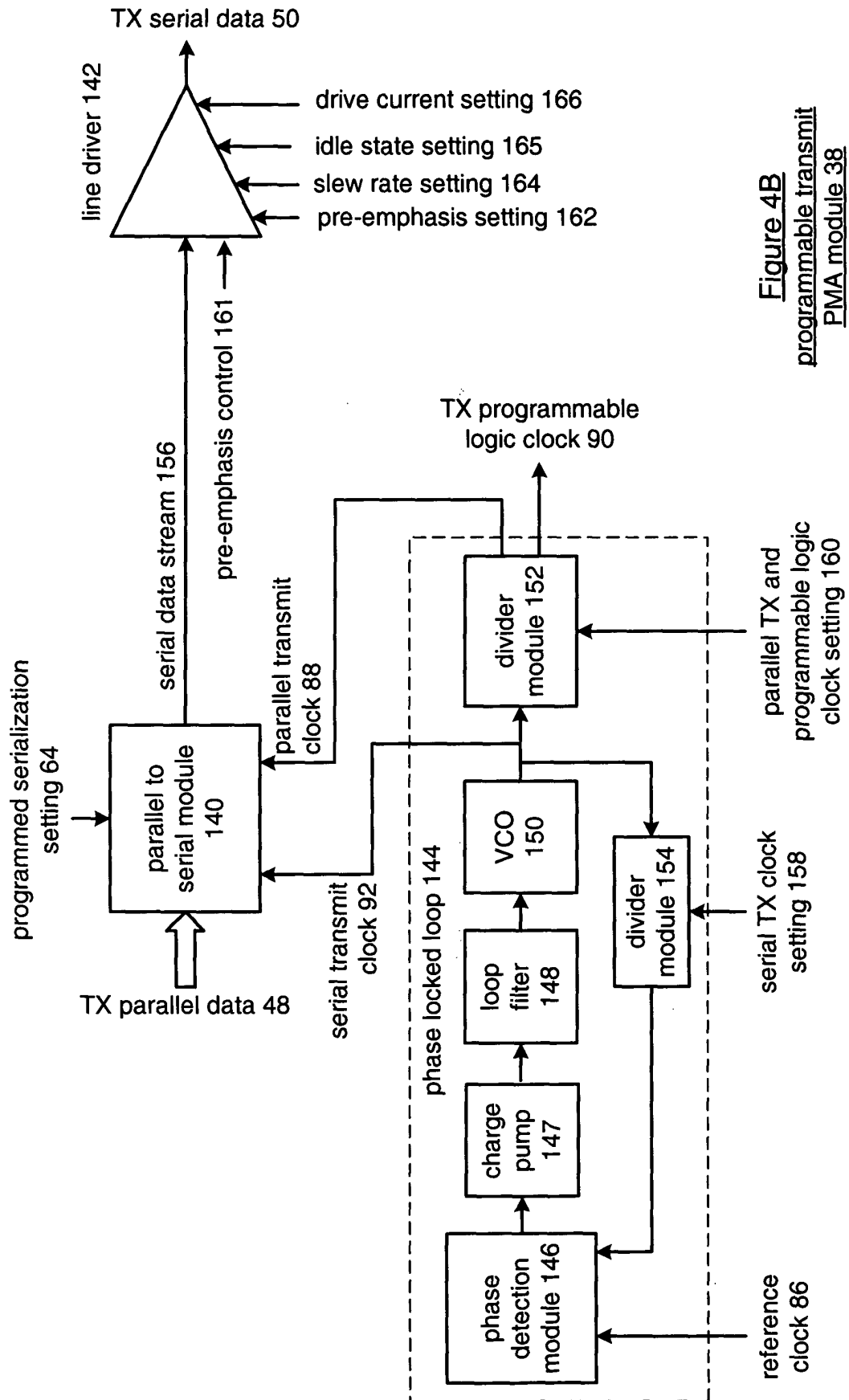
**Figure 2**  
programmable MGT 14 - 28



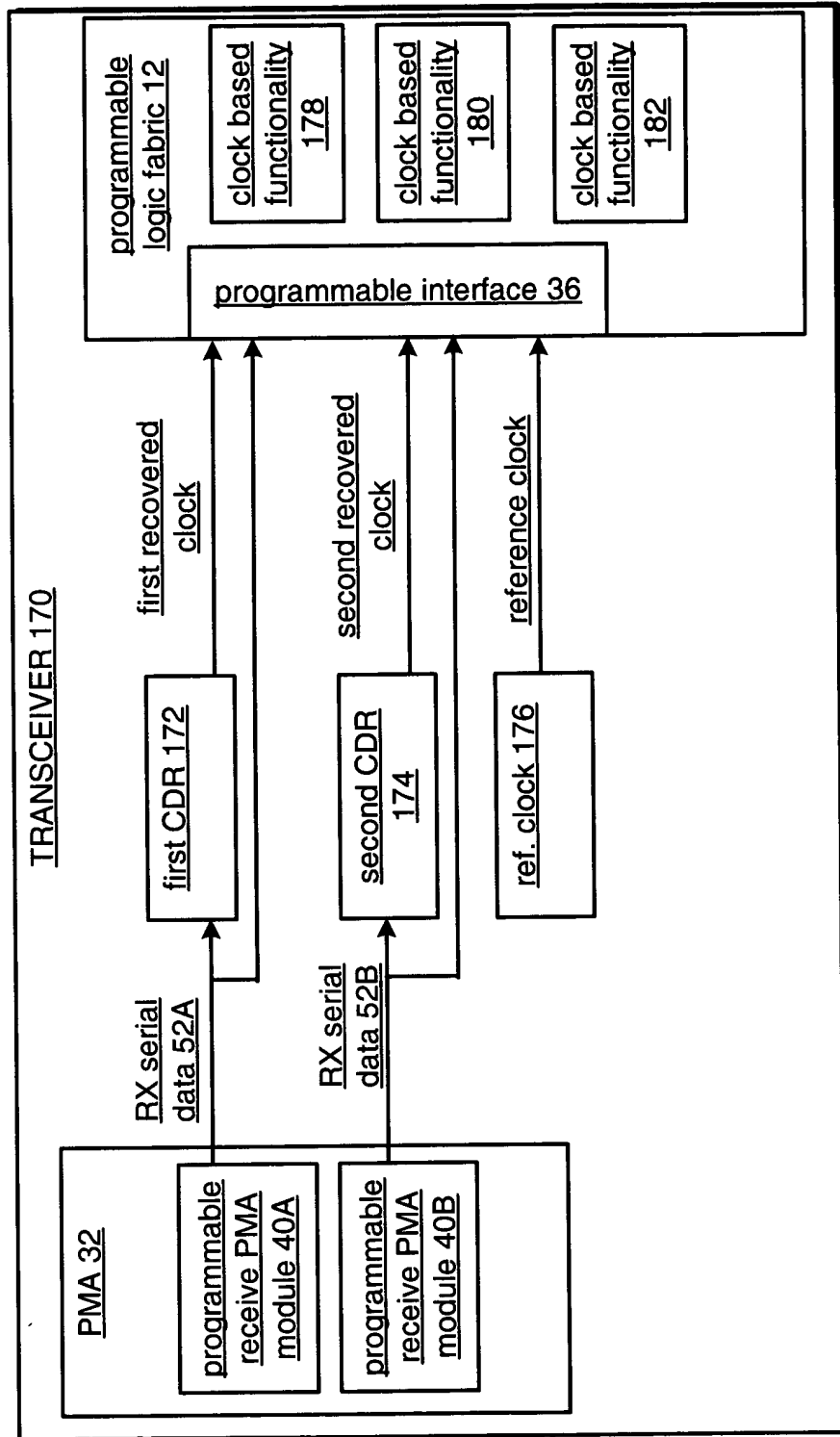
**Figure 3**  
programmable MGT 14 - 28



**Figure 4A**  
programmable receive  
PMA module 40



**Figure 4B**  
programmable transmit  
PMA module 38



**Figure 5**  
transceiver 170

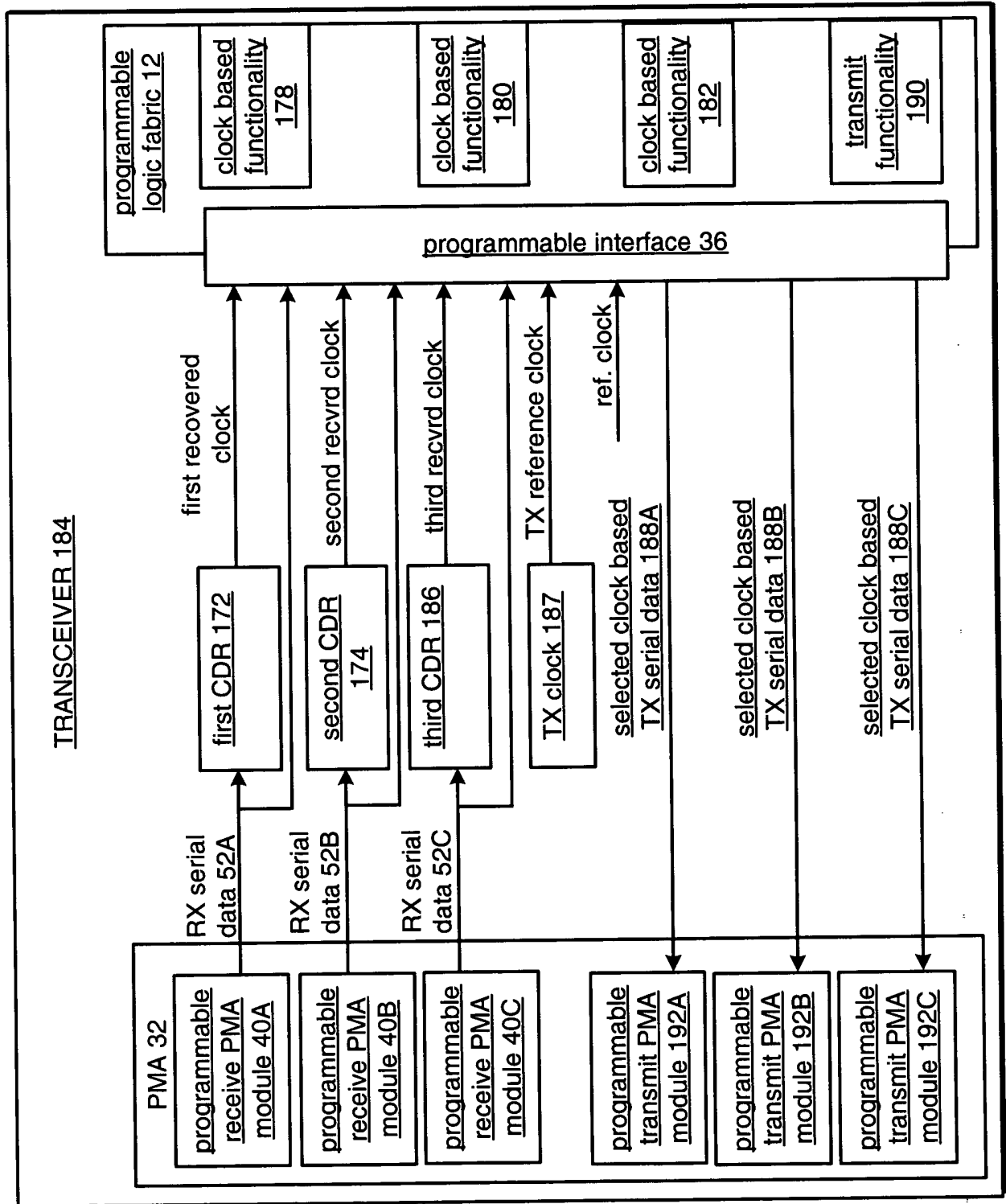


Figure 6  
transceiver 184

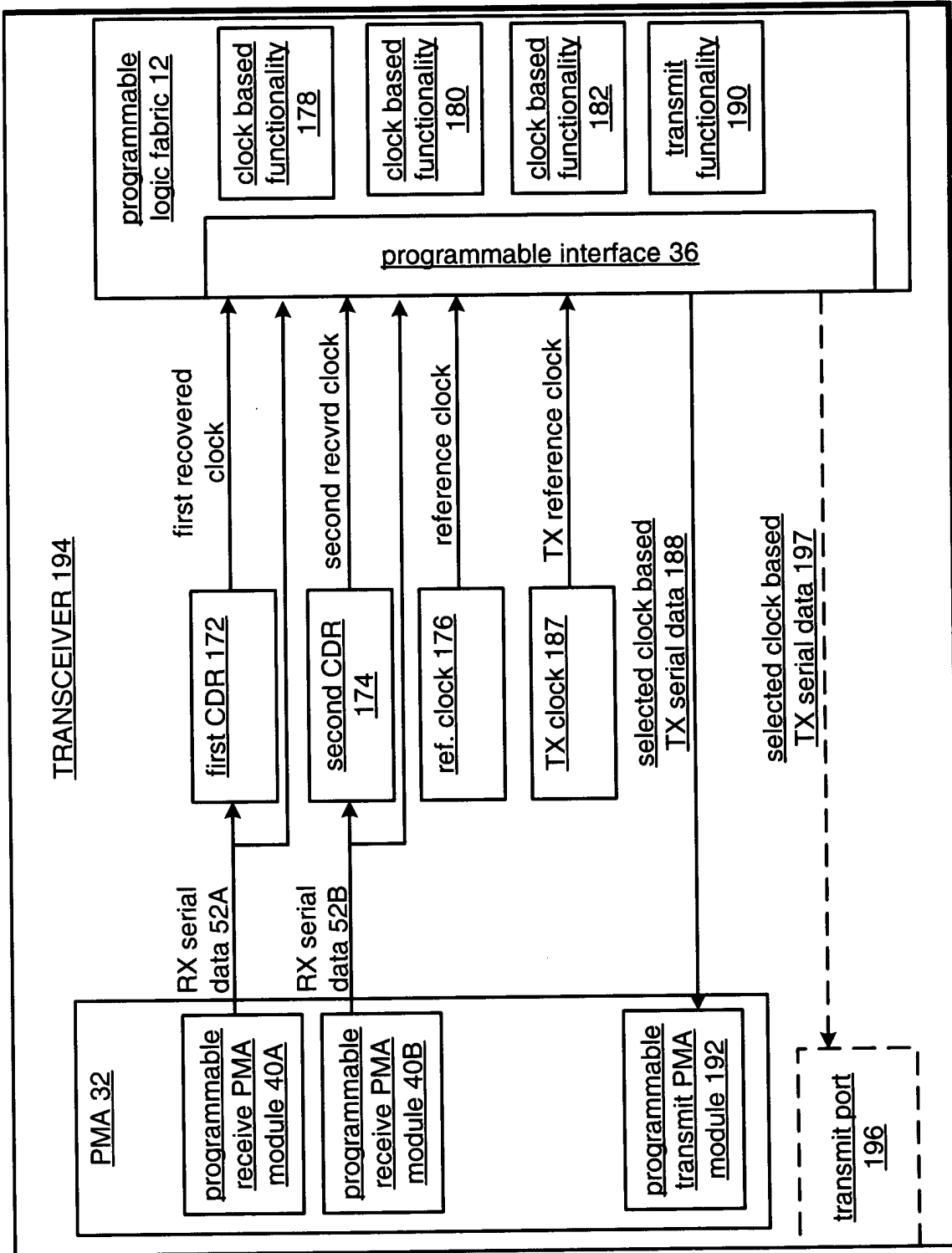


Figure 7  
transceiver 194



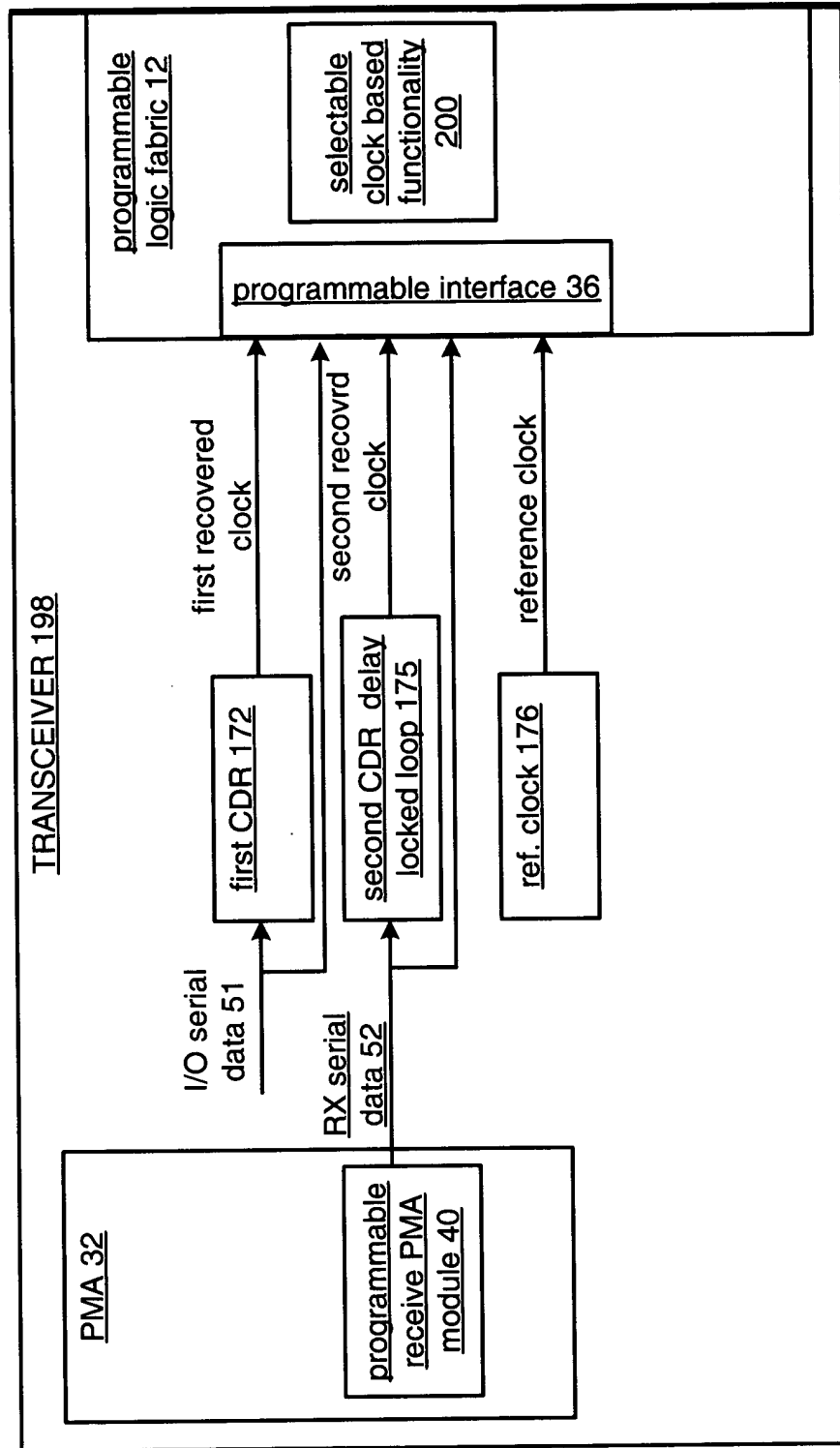
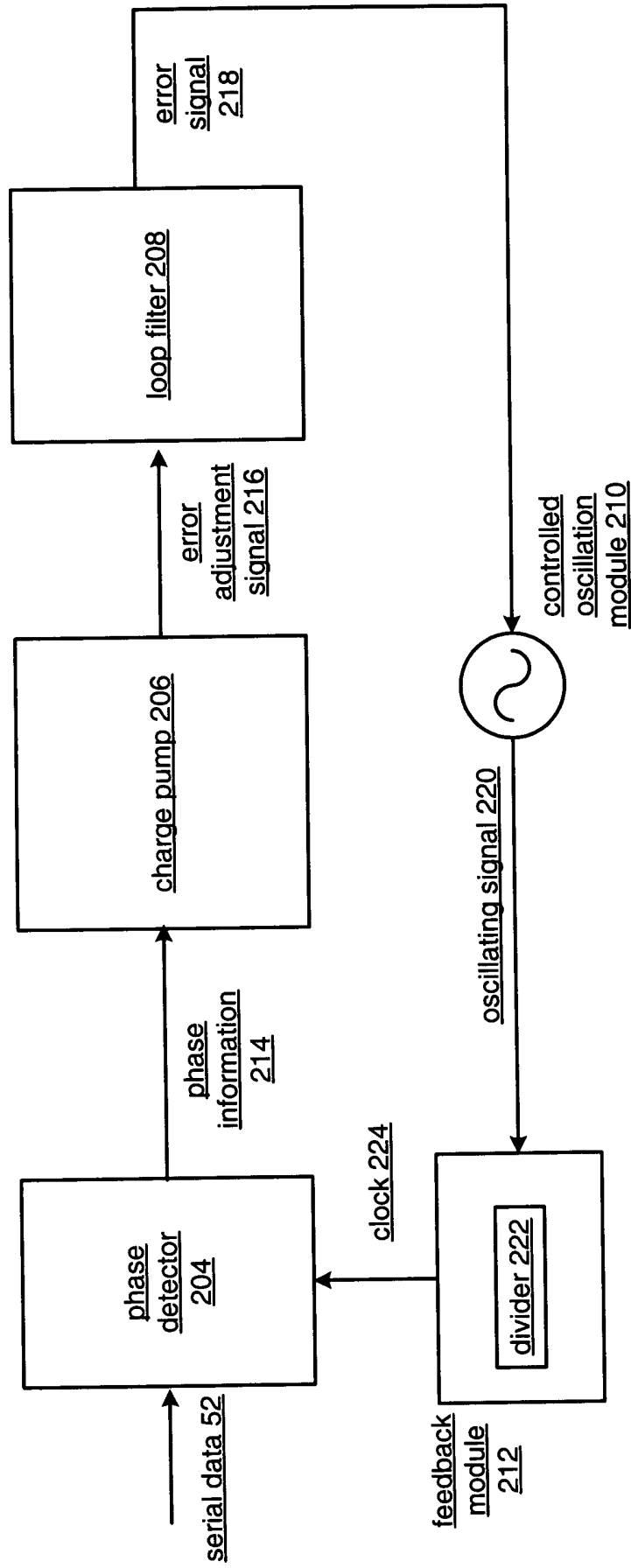
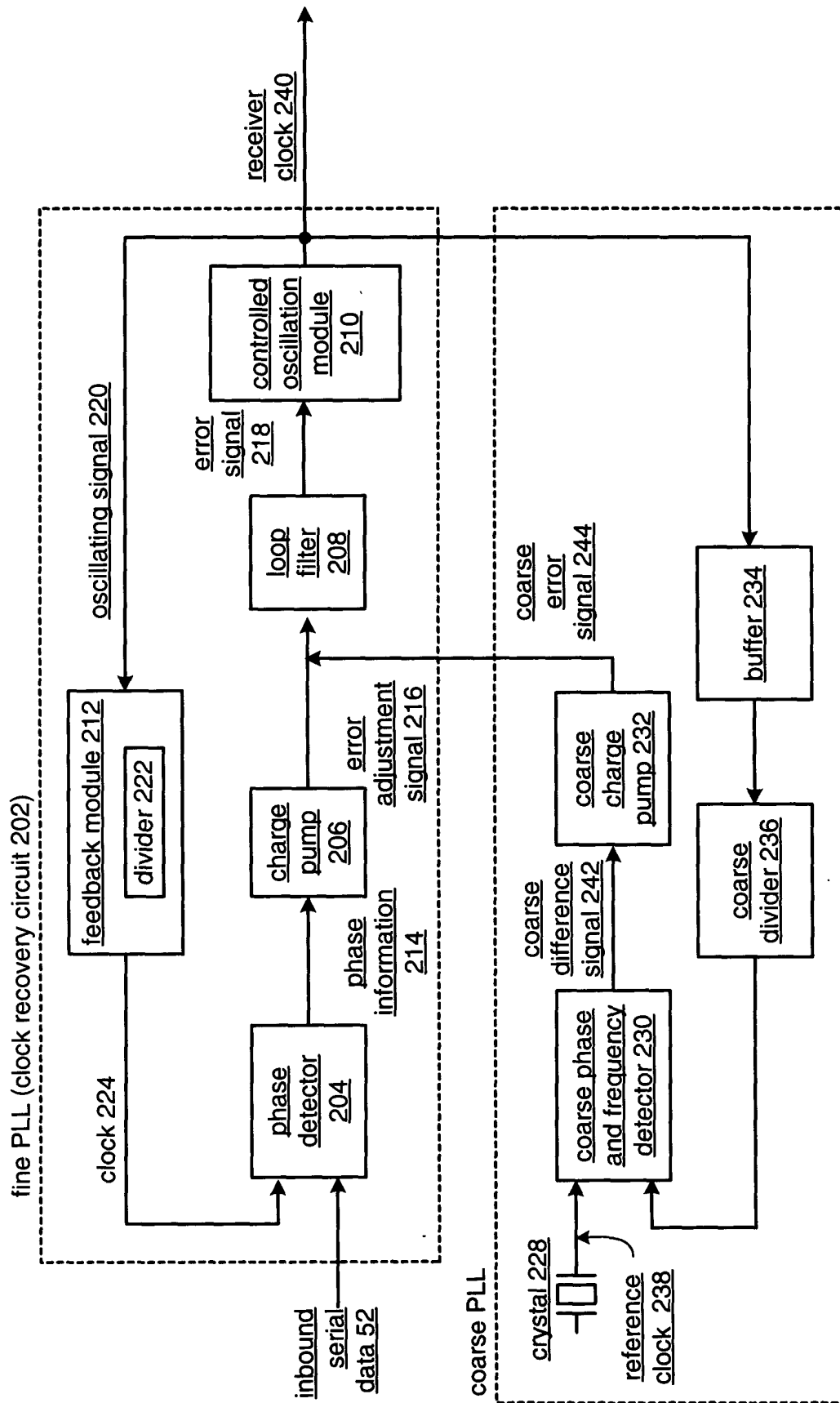


Figure 8  
transceiver 198



**FIG. 9**  
clock recovery circuit 202



**FIG. 10**  
clock data recovery block 226

